

Docket No.: 240708US2/sao

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Kenichi HAYASHI, et al.

SERIAL NUMBER: 10/625,583

GROUP: 2826

FILED: July 24, 2003

EXAMINER: WILLIAMS,  
ALEXANDER O

FOR: SEMICONDUCTOR DEVICE AND SEMICONDUCTOR ASSEMBLY MODULE  
WITH A GAP- CONTROLLING LEAD STRUCTURE

**REQUEST TO CORRECT ALLOWED CLAIMS**

MAIL STOP ISSUE FEE  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request  
correction of your records to reflect the correct allowed claims.

The allowed claims should be: **1-18, 20.**

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



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